

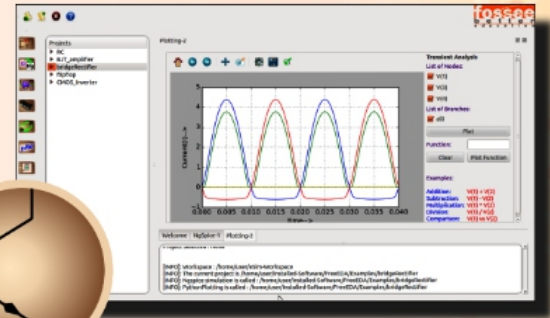
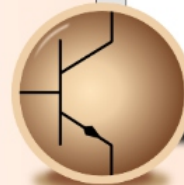


Mixed Signal Circuit Design and Simulation

# Marathon

using eSim

**Register**



The purpose of the Mixed Signal Circuit Design and Simulation Marathon using eSim is to provide an open platform to students to innovate, create state-of-the-art design and integrate it into real-world applications. This marathon is based on eSim, an open-source EDA tool for circuit design, simulation & PCB design, developed by FOSSEE, IIT Bombay. eSim is an open-source alternative to PSpice, OrCAD

**Registration: 10 - 24 February 2022 | Marathon: 25 February - 17 March 2022**

## Timeline

Registration for the Marathon

**10 - 24 Feb 2022**

Marathon Inauguration Webinar

**25 Feb 2022**

Literature Survey

**25 - 28 Feb 2022**

Report Submission

**28 Feb 2022**

Implementation

**1 - 6 Mar 2022**

Report Submission & documentation

**7 - 10 Mar 2022**

Result Declaration

**17 March 2022**